

DragonFlyBSD - Bug #2697

i386 MP locks up during boot after MI interrupts, TSC related?

07/07/2014 10:14 PM - davshao

Status:	Resolved	Start date:	07/07/2014
Priority:	Normal	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:			
Description			
On three systems running i386 3.9-DEVELOPMENT with either multiple cores or with hyperthreading, following commit 1a1ada88346625b2b581e0676eea4df99f1b89b6 Date: Sun Jul 6 12:49:22 2014 -0700 boot - Fix timecount issues with some BIOSes (haven't completely bisected, might be the previous commit) booting freezes some time after Initialize MI interrupts For example on an Intel Atom: Initialize MI interrupts objcache: magazine cap [13, 141] Finish MP startup Active CPU Mask: 00000003 On a Asrock Z77M (Intel i3): Initialize MI interrupts objcache: magazine cap [13, 141] Finish MP startup Active CPU Mask: 0000000f TSC testing MP synchronization TSC is MP synchronized, after 57322 tries On the other hand, the problem does not manifest on a true single processor i386 Pentium 4 machine.			

History

#1 - 07/08/2014 11:51 PM - davshao

DragonFly i386 3.9-DEVELOPMENT now boots successfully on an Asus M5A87 multicore machine and a Asrock Z77M Intel i3 multicore machine and an Lenovo S10 Ideapad netbook with an Intel Atom with hyperthreading, as well as retaining the previously successful booting on a single processor Pentium 4 machine and in a virtual machine in VMWare Fusion 6.

commit b3b03e93a528676294dc8f6bac1d6a3270de43c8
Date: Tue Jul 8 11:17:48 2014 -0700

kernel - (32 bit) fix MP boot problems

thus appears to have completely resolved this issue.

#2 - 11/05/2014 12:13 AM - dragonflybsd1

- Status changed from New to Resolved